Form PTO-1449 (Modified) Serial No.: Atty Docket No.: 09/733,443 04148P011 List of Patents and Publications Statement **Applicant:** Steven McDonagh (Use several sheets if necessary) Filing Date: December 7, 2000 Page 1 of 1 REFERENCE DESIGNATION **U.S. PATENT DOCUMENTS** Class Sub-Filing date Document No. Examiner if appropriate Initials Class FOREIGN PATENT DOCUMENTS No. Document No. Date Country. Class Sub-Trans-Class lation OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.) "Optimization of Phase-Locked Loop Performance In Data Recover Systems", Ramos S. Co and J.H. Mulligan, IEEE Journal of Solid-State Circuits, vol. 29, pp 1022-1034, September 1994. " Modeling and Characterization of the 3rd Order Charge-Pump PLL: a Fully Event Driven Approach", Analog Integrated Circuits and Signal Processing, An International Journal, Vol. 19, No. 1, (1999), pp. 25-45. Christian D. Hedayata, Ahmed Hachem, Yves Leduc and Gerad Benbassat. **Date Considered** Examiner -JAISON JOSEPH EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.